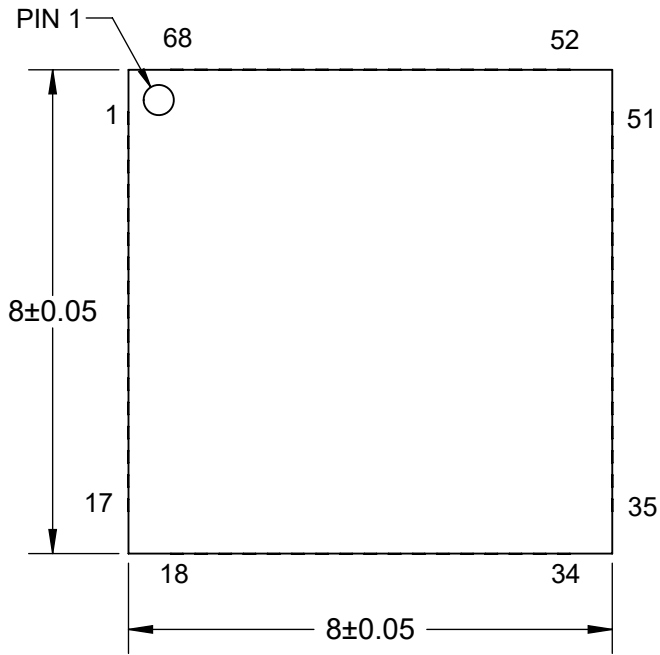
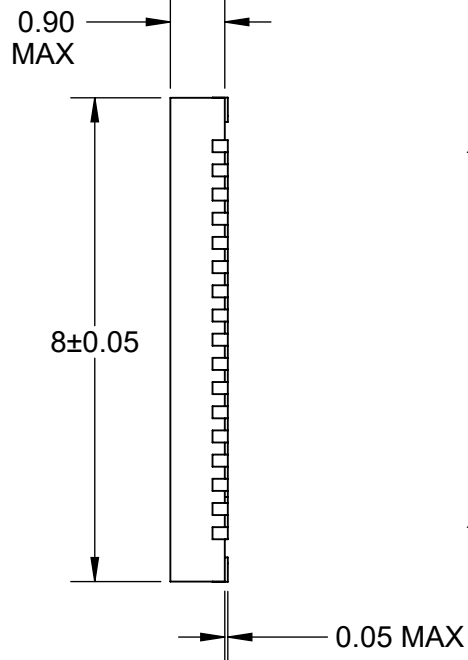


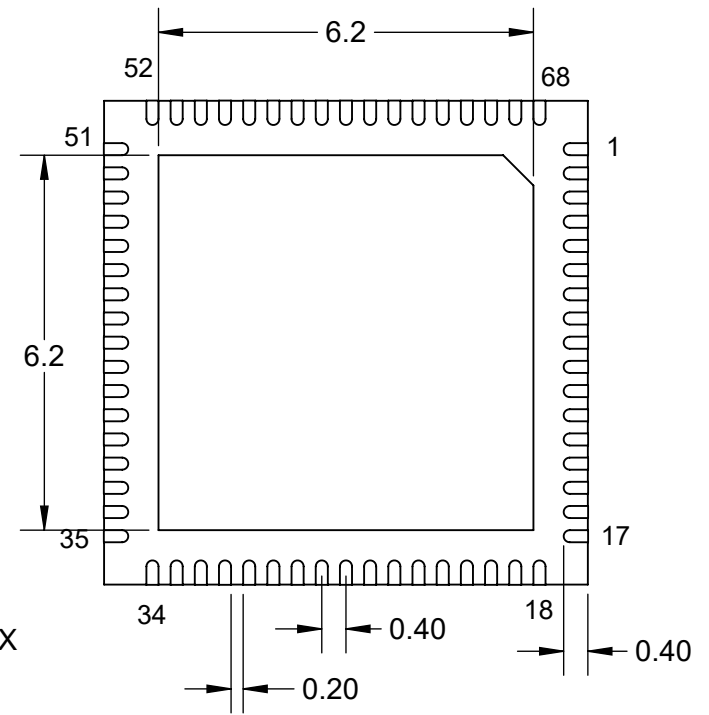
**TOP VIEW**



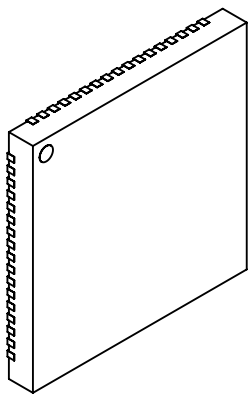
**SIDE VIEW**



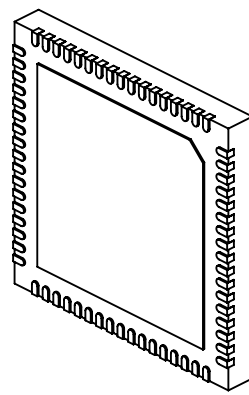
**BOTTOM VIEW**



**MODEL**



**TOP**



**BOTTOM**

**Notes: (Unless Otherwise Specified).**

- 1) BODY: PLASTIC, SEMICONDUCTOR GRADE.
- 2) LEAD FRAME: COPPER, C-194 F/H.
- 3) LEAD FRAME PLATING: NiPdAu.
- 4) FRAME THICKNESS: 0.203mm.
- 5) DIE PAD: 6.2 x 6.2mm MAX EXPOSED PAD.
- 6) JEDEC OUTLINE: MO-220.
- 7) DIMENSIONS mm.

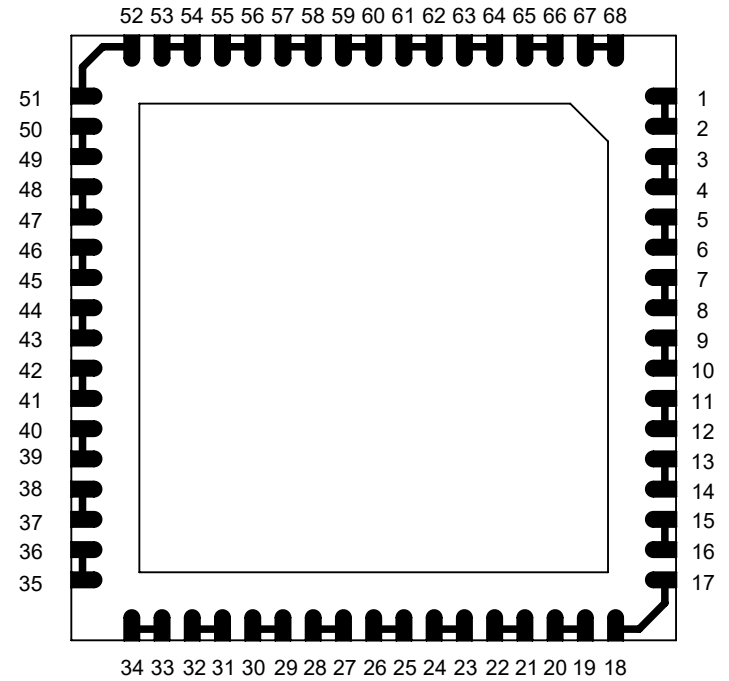
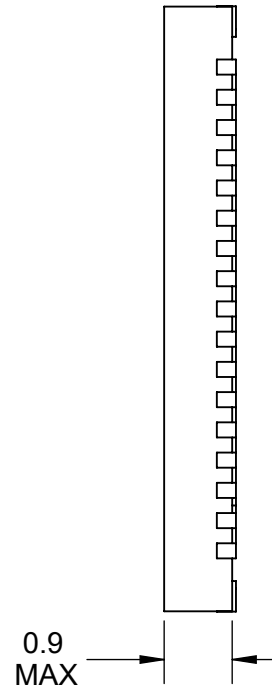
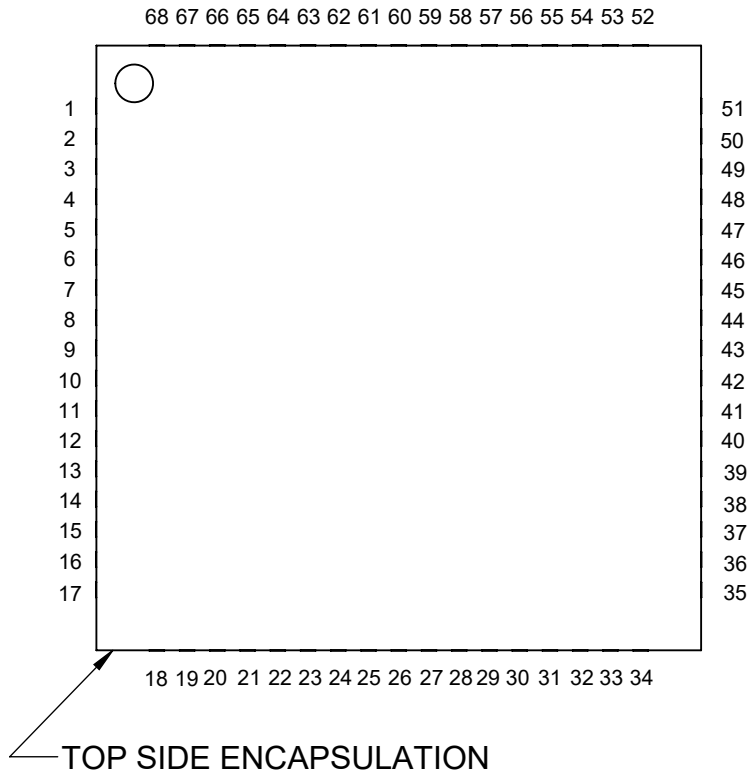
APPROVALS	DATE	<b>TopLine®</b>			
DRAWN T. Au	4/30/2023				
ENG M. Hart	4/30/2023	TITLE 68-LEAD 8mm P0.4mm QFN DAISY CHAIN			
MFG		SCALE 8:1	SIZE A	DRAWING NO. 446800	REV A
QA					
CUST		DO NOT SCALE DRAWING			SHEET 1 OF 5
REVISED					

# DAISY CHAIN PATTERN

# BOTTOM VIEW

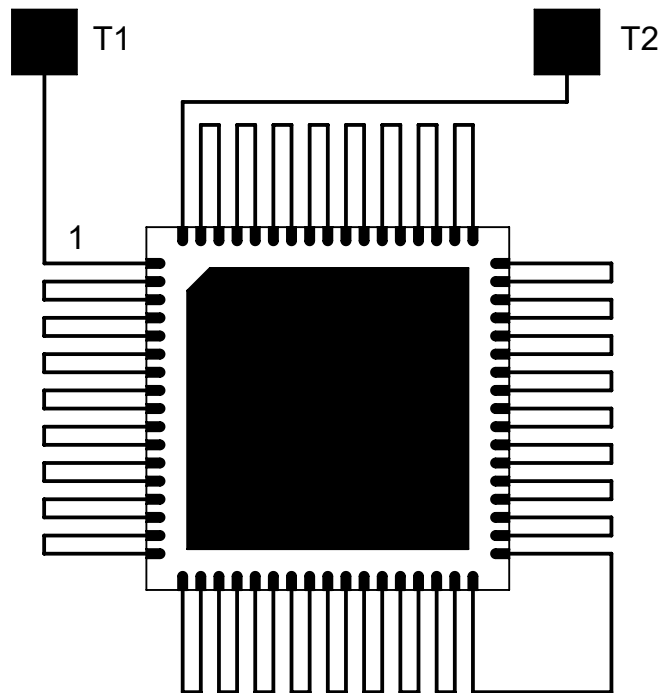
## TOP VIEW

## SIDE VIEW

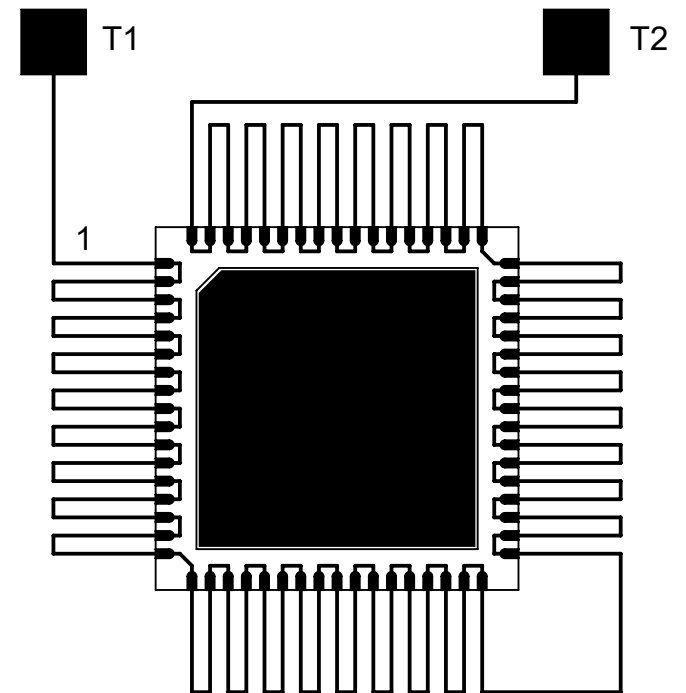


NOTE:  
1. PACKAGE DAISY CHAIN BY WIRE BONDING TO INTERNAL BOND PADS.

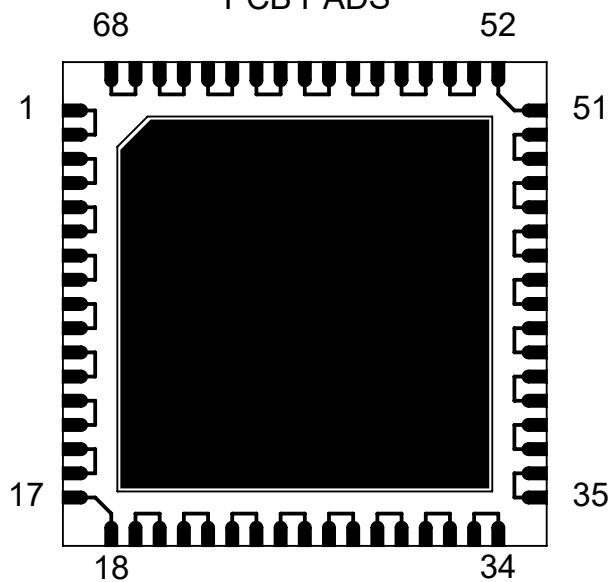
<b>TopLine<sup>®</sup></b>			
TITLE    68-LEAD 8mm P0.4mm QFN DAISY CHAIN			
SCALE	SIZE	DRAWING NO.	REV
10:1	A	446800	A
DO NOT SCALE DRAWING			SHEET 2 OF 5



RECOMMENDED  
PCB PADS




AFTER MOUNTING  
ON PCB

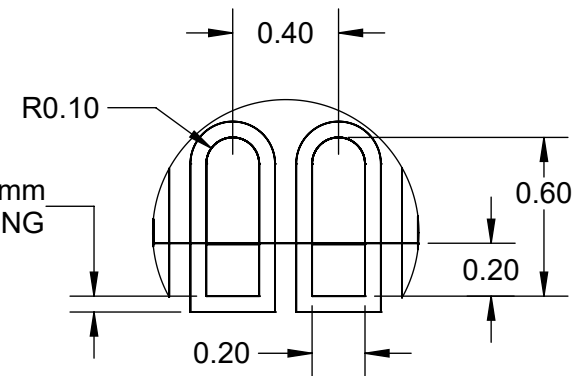
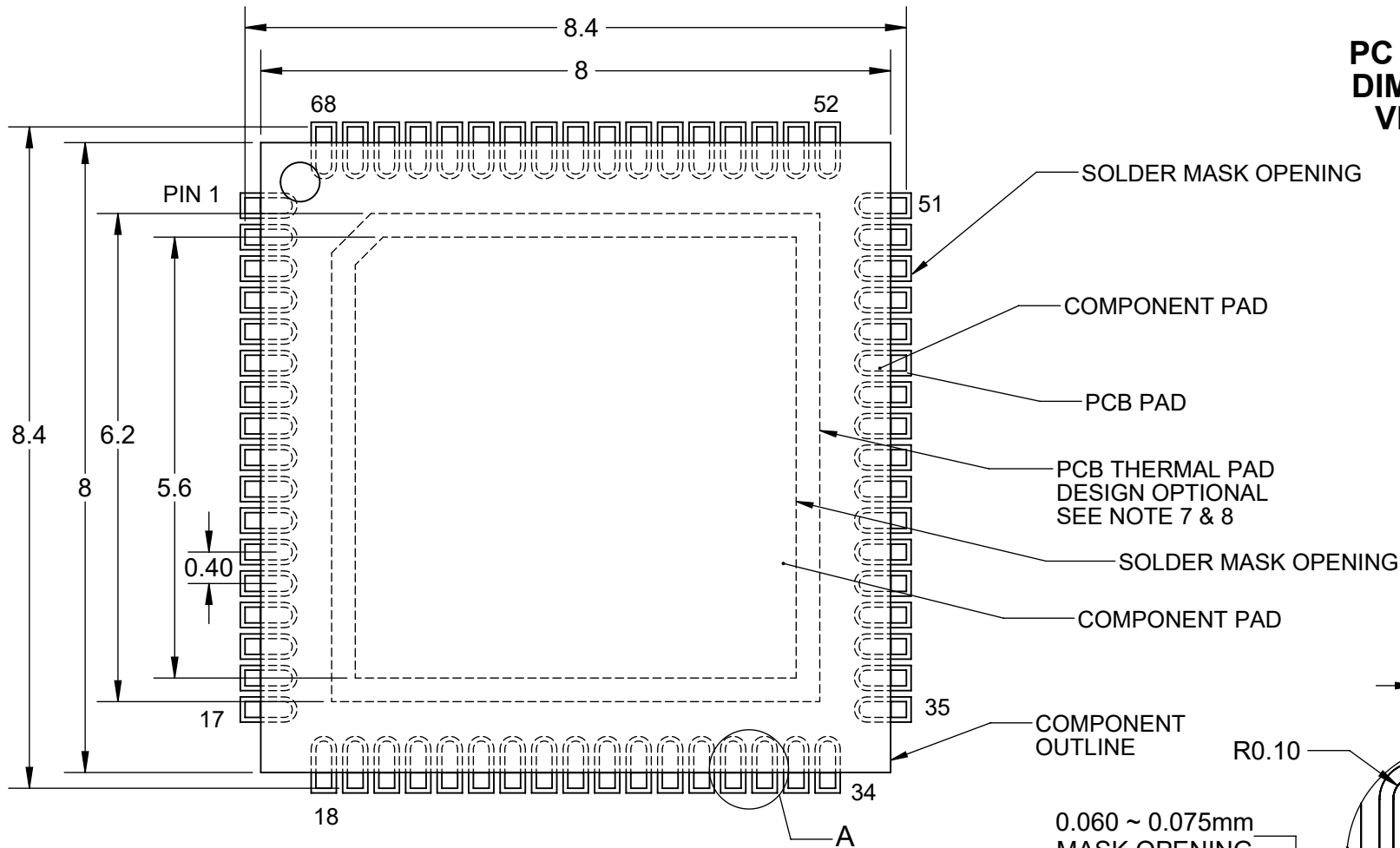


VIEW FROM TOP

DAISY CHAIN NET LIST		
PINS	PINS	PINS
1 ~ 2	3 ~ 4	5 ~ 6
7 ~ 8	9 ~ 10	11 ~ 12
13 ~ 14	15 ~ 16	17 ~ 18
19 ~ 20	21 ~ 22	23 ~ 24
25 ~ 26	27 ~ 28	29 ~ 30
31 ~ 32	33 ~ 34	35 ~ 36
37 ~ 38	39 ~ 40	41 ~ 42
43 ~ 44	45 ~ 46	47 ~ 48
49 ~ 50	51 ~ 52	53 ~ 54
55 ~ 56	57 ~ 58	59 ~ 60
61 ~ 62	63 ~ 64	65 ~ 66
67 ~ 68		

			
TITLE		68-LEAD 8mm P0.4mm QFN DAISY CHAIN	
SCALE	SIZE	DRAWING NO.	REV
6:1	A	446800	A
DO NOT SCALE DRAWING			SHEET 3 OF 5

# PC BOARD LAYOUT DIMENSIONS IN MM VIEW FROM TOP



DETAIL A  
SCALE 35 : 1

Notes: (Unless Otherwise Specified).

- 1) DIMENSIONS ARE PRESENTED ONLY AS A GUIDELINE. DESIGNERS SHOULD USE THEIR OWN KNOWLEDGE BASE WHEN DESIGNING THE PCB.
- 2) SURROUND EACH SIDE OF I/O PERIMETER PADS WITH 0.060~0.075 mm (NSMD) SOLDER MASK OPENING (2.4~3.0mils) OPTIONALLY OK TO USE RECTANGLE (NSMD) MASK OPENING AROUND I/O PADS.
- 3) ROUNDED PCB LAND PADS REDUCE SOLDER BRIDGING. PAD CHAMFER ANGLE MAY VARY.
- 4) PCB LANDS SHOULD BE 0.2mm LONGER THAN THE PACKAGE I/O PADS.
- 5) THE WIDTH OF PERIMETER PCB PADS SHOULD MATCH (1:1) THE WIDTH OF THE PACKAGE PADS.
- 6) REFER TO INDUSTRY REFERENCES SUCH AS IPC-SM-782 FOR PCB LAND PATTERN DESIGN.
- 7) THERMAL GROUND PADS MAY BE CHANGED TO SUITE REQUIREMENTS OF THE DESIGNER.
  - A) MAKE COPPER THERMAL PAD AS LARGE AS POSSIBLE.
  - B) DRILL MULTIPLE THERMAL VIAS 0.25~0.33mm DIAMETER USING 0.8~1.2mm PITCH GRID.
  - C) PLATE THERMAL VIA BARRELS WITH 1-OUNCE COPPER (18 $\mu$ m).
  - D) TENT (COVER) THERMAL VIAS WITH SOLDER MASK 0.1mm LARGER THAN THE VIA DIAMETER.
- 8) STENCIL DESIGN MAY BE CHANGED TO SUIT REQUIREMENTS OF THE DESIGNER.
  - A) LASER CUT STENCIL 0.125mm (5mil) THICK. APERTURE SIZE-TO-LAND RATIO OF 1:1.
  - B) THE SOLDER PASTE OPENING IN THE THERMAL PAD AREA SHOULD BE A MATRIX ARRAY OF SMALLER APERTURES INSTEAD OF ONE LARGE APERTURE TO CONTROL PASTE AMOUNTS.
  - C) APPLY 50% TO 80% SOLDER PASTE COVERAGE IN THE PAD AREA.

**TopLine**<sup>®</sup>

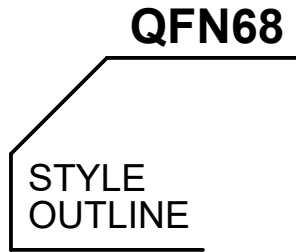
TITLE 68-LEAD 8mm P0.4mm  
QFN DAISY CHAIN

SCALE	SIZE	DRAWING NO.	REV
8:1	A	446800	A

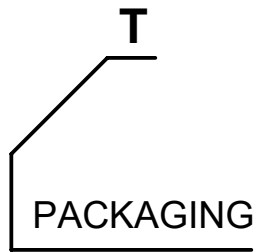
DO NOT SCALE DRAWING

SHEET 4 OF 5

## PART NUMBERING SYSTEM



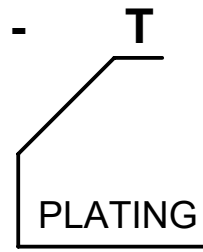
QUAD FLAT  
NO LEAD  
68-LEADS



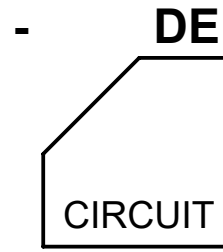
T = TRAY  
E = CUT TAPE  
E7A = 7" REEL  
M = TUBE



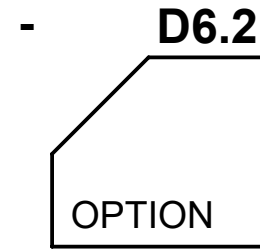
.4 = 0.4MM



T = Matte  
Tin Sn



DE = DAISY CHAIN  
ISO = ISOLATED  
BLANK = DUMMY



BLANK = NO DIE  
D = DUMMY DIE  
6.2 = Ground Pad  
6.2 x 6.2mm

PART NUMBER	DAISY CHAIN	PACKAGING	RoHS Pb-FREE	Plating	DUMMY DIE
QFN68M.4-T-DE-D6.2	YES	TUBE	YES	Sn	YES
QFN68T.4-T-DE-D6.2	YES	JEDEC TRAY	YES	Sn	YES
QFN68E.4-T-DE-D6.2	YES	TAPE	YES	Sn	YES

OTHER PART NUMBER COMBINATIONS AVAILABLE. CONTACT TOPLINE.

<b>TopLine®</b>			
TITLE 68-LEAD 8mm P0.4mm QFN DAISY CHAIN			
SCALE NONE	SIZE A	DRAWING NO. 446800	REV A
DO NOT SCALE DRAWING			SHEET 5 OF 5