

## ***DNP - Distance from Neutral Point*** ***Calculating DNP on CCGA***

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DNP is an acronym for "**D**istance from **N**eutral **P**oint".

The maximum DNP is the linear distance measured from the center of the package to the corner pads.

Units of DNP can be either metric (mm or um) or imperial (inches or mils), as long as it is consistent with other units of measures in the stress (strain) formula shown on page 3.

The larger (longer) the DNP, the more stress (strain) will be experienced at the pads, columns or balls on an array package.

Failures (delamination) caused by stress (strain) are typically the pads at the corners of the substrate located furthest from the center, as well as the corners under the silicon die (or flip-chip).

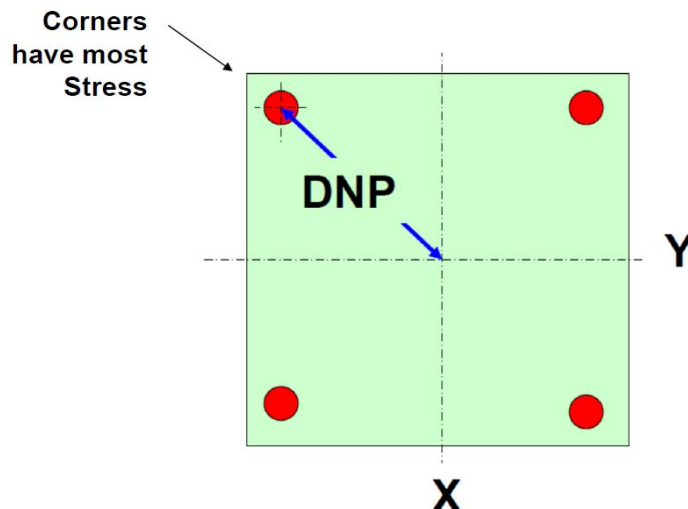
By way of example, let's assume the substrate (X or Y) is 35 x 35mm.

For square substrates, the DNP is one-half of X (or Y) multiplied by the square root of 2.

Therefore, DNP to the corners of a 35mm square package is roughly 24.75mm (rounded)

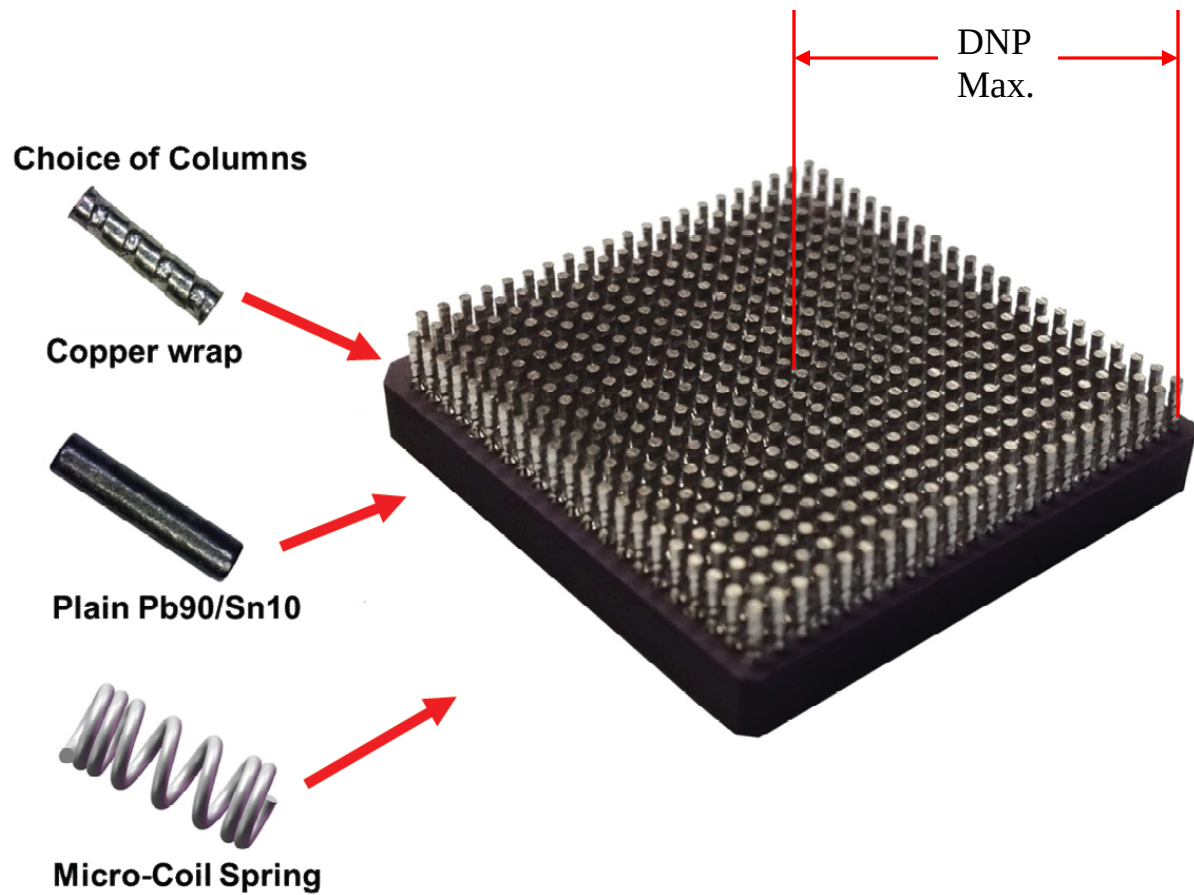
$$\text{DNP} = \sqrt{2} * (X/2)$$

## **Distance from Neutral Point** **Typically from the Center**



***Maximum DNP***  
***Maximum Distance From Neutral Point***

The maximum DNP is the linear distance from the center of the package to the corners.



### ***What causes stress (strain)?***

Stress (Strain) is created whenever different materials are joined together over varying temperatures.

Solder columns absorb stress between ceramic packages and the PC board.

Ceramic has a coefficient of thermal expansion (CTE) of  $\sim 7.5\text{ppm}/^{\circ}\text{C}$ .

PC boards have a CTE of roughly  $\sim 17.5\text{ppm}/^{\circ}\text{C}$

This causes a  $10\text{ppm}/^{\circ}\text{C}$  difference between the ceramic module and the PC board.

The larger the DNP (Distance from Neutral Point) of ceramic module, the more stress (strain) is created at the solder joints.

CBGA solder ball (joints) stretch and squeeze as temperatures change from hot to cold.

Eventually, the solder ball will delaminate - either at the ceramic package or at the PC board.

Let's look at the equation, to understand more about stress (strain).

$$\Delta x = DNP * \Delta T * (CTE_1 - CTE_2)$$

***DNP*** – Distance from Neutral Point

***$\Delta T$***  - Temperature change

***$CTE_1$***  – Glass-Epoxy (X/Y $\sim 17\text{ppm}$  @  $25^{\circ}\text{C}$ ) Z-Axis  $> 40\text{ppm}$

***$CTE_2$***  – Ceramic – Uniform across material

**$\Delta x$**  is the change in stress (strain).

DNP is the distance from the "NEUTRAL POINT" - measured from the center of the component.

**$\Delta T$**  is the change in the temperature (for example minus  $-40^{\circ}\text{C}$  to  $+100^{\circ}\text{C}$ , the total is  $140^{\circ}\text{C}$ )

CTE is the coefficient of thermal expansions found in the data sheets of the materials.

**"SAFE HARBOR"**  
**When it is safe to use of CBGA Ball Array?**  
**When is it required to use a CCGA - Column Grid Array?**

| Component Size | $\Delta T$<br>$\pm 75^{\circ}\text{C}$ | $\Delta T$<br>$\pm 125^{\circ}\text{C}$ | $\Delta T$<br>$\pm 165^{\circ}\text{C}$ |
|----------------|----------------------------------------|-----------------------------------------|-----------------------------------------|
| <15 x 15mm     | BGA                                    | BGA                                     | BGA                                     |
| 25 x 25mm      | BGA                                    | BGA<br>Use with<br>Caution !            | CCGA                                    |
| 31 x 31mm      | BGA<br>Use with<br>Caution !           |                                         |                                         |
| 35 x 35mm      |                                        |                                         |                                         |
| >40 x 40mm     | CCGA                                   | CCGA                                    | CCGA                                    |

"GREEN" represents the safe use of CBGA - ceramic ball grid array.

It is generally safe to use CBGA with package size 15mm or smaller.

Small CBGA components will not experience a significant CTE mismatch.

Exercise caution if the ceramic component is 25mm or more, especially if the operating temperature will swing more than +/- 125 °C.

Large CBGA will fail (electrical delamination) after multiple thermal cycles of +/- 125 °C.

It is recommended to use CCGA (Column Grid Array) when ceramic packages exceed 27mm square.

For more information, visit **www.CCGA.co** (not .com)