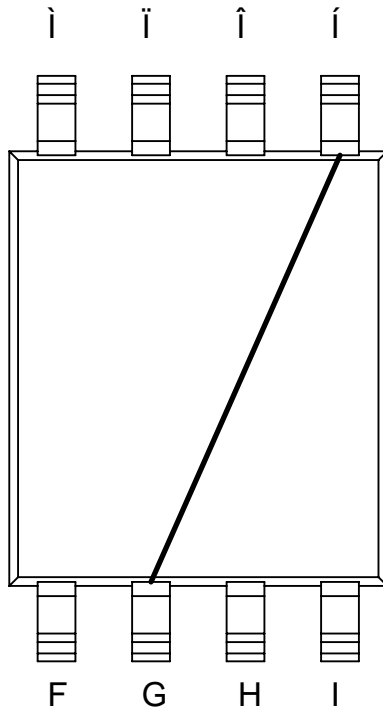
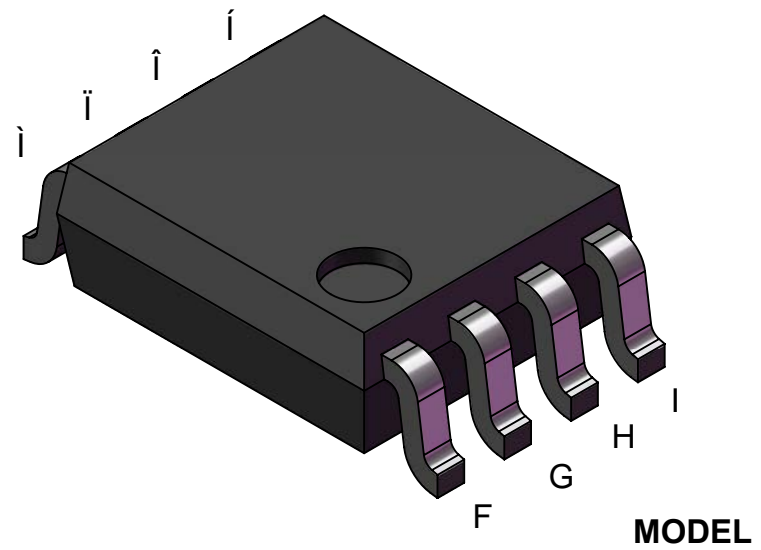


INTERNAL WIRING CIRCUIT (JUMPER) NUMBER SUFFIX TOP VIEW

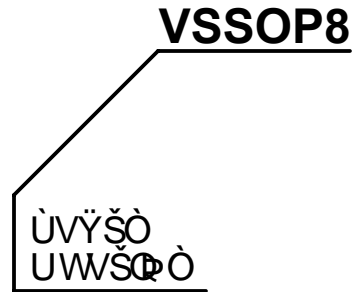


DC52
 RVT UOU
 UOAGAI

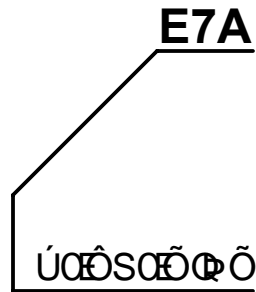


TopLine®			
varšò		XÙÙÙÙÙ ÆÓÍ G RVT UOUAJCAGAI	
UOASÒ	Uazò	ÖÜÆY ØÓÆUE	Üòx
FÍ K	CE	FGè Í G	CE
ØUÆUVUÜOASÒØÜÆY ØÓ			ÜPÖÖVÁGUZÁ

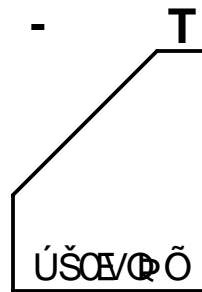
PART NUMBERING SYSTEM



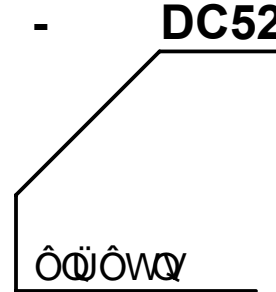
XÙÙÙ
Ì Áġ •



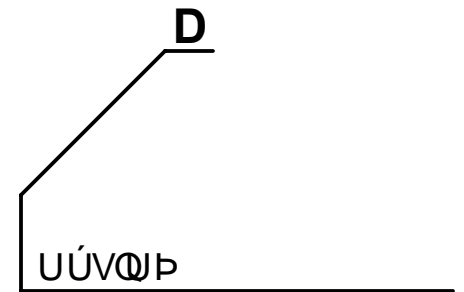
ÒĪ ÇĀĀ ĀÛÒÒŠ



VĀM
ÚĀĒÇÙÒÒ
ÛĪ PÛ
ŠÒÇĀÛÒÒ
ÛĪ FĒĒ
T ÇÈ/VÒĀÇ



RWT ÚÒÛ
ÔÔĪ ĠĒÛÇĀĪ
ÔÛPÇÓÓVÒ

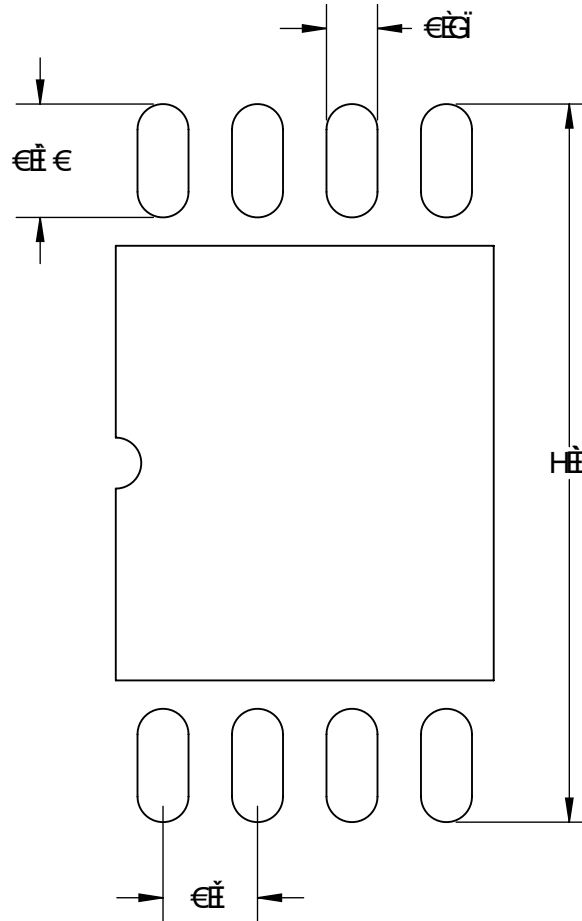


ÖĀÇ VÒÛPÇŠÖÇ

PART NUMBER	SHORTED PINS	NOTES	REEL QTY	MSL LEVEL
XÙÙÙ ÒĪ ÇÈ/VÒĀÇ	ĠĀĀ	RWT ÚÒÛ	HĒĒĒ	F

VĀŠÒ		XÙÙÙĪ ĠĒÔĪ Ġ RWT ÚÒÛÇĀĪ	
ÚÇŠÒ PÛPÒ	ÚÇÒ Ç	ÛÛÇ ÇĒÇPĒ FĠĪ Ī Ġ	ÛÒX Ç
ÛÛÇPÛVĪÚÇŠÒĒÛÇ ÇÈ			ÛPÓVĀĪÛÇĀ

PCB BOARD LAND PATTERN



Π[c • KVM | • • Ά @ ! , ā ^ Ά] ^ & a a d
 FE 00 0 P U 0 P U A E U 0 P A { E
 GE U 0 0 A U 0 E J 0 0 0 0 P U 0 P U A E J 0 A J U 0 U 0 P V 0 0 A P S Y A E U A Z 0 W 0 0 S P 0 E
 HE 0 0 U 0 P 0 U U A P U W S 0 A M U 0 A P 0 0 A J Y P A 0 Y U 0 U 0 P 0 0 A P 0 P A 0 0 U 0 P 0 0 A J 0 0 E

TopLine®			
ναρσο		XUUVI EOI G RVT UOUA J P A I	
UOCASO	UZO	OUAY P O A U E	UOX
G K	CE	FGI G	CE
OU A U V A U O A S O O U C E P O			UPOVA A J Z A

LEAD FREE REFLOW PROFILE OF SMD PACKAGE



DESCRIPTION	REQUIREMENT
ÚÓÜÆΜÜÓ ÚÓÜÆΜÜÓ	H° ÓÁÚÚÓÇS ÚÓÜÆΜÜÓ
ÚÓÜÆΜÜÓ ÚÓÜÆΜÜÓ	Fí €° ÓÁÚÚÓÇS ÚÓÜÆΜÜÓ
ÚÓÜÆΜÜÓ ÚÓÜÆΜÜÓ	Í €ÁÚÚÓÇS ÚÓÜÆΜÜÓ
ÚÓÜÆΜÜÓ ÚÓÜÆΜÜÓ	H° ÓÁÚÚÓÇS ÚÓÜÆΜÜÓ
VQ ÓÁÚÚÓÇS ÚÓÜÆΜÜÓ	Í €ÁÚÚÓÇS ÚÓÜÆΜÜÓ
ÚÓÜÆΜÜÓ ÚÓÜÆΜÜÓ	Gí €° ÓÁÚÚÓÇS ÚÓÜÆΜÜÓ
VQ ÓÁÚÚÓÇS ÚÓÜÆΜÜÓ	GEÁÚÚÓÇS ÚÓÜÆΜÜÓ
ÚÓÜÆΜÜÓ ÚÓÜÆΜÜÓ	Í €ÁÚÚÓÇS ÚÓÜÆΜÜÓ
VQ ÓÁÚÚÓÇS ÚÓÜÆΜÜÓ	Í €ÁÚÚÓÇS ÚÓÜÆΜÜÓ

TopLine®

VQ ÓÁÚÚÓÇS ÚÓÜÆΜÜÓ

ÚÓÜÆΜÜÓ	ÚÓÜÆΜÜÓ	ÚÓÜÆΜÜÓ	ÚÓÜÆΜÜÓ
ÚÓÜÆΜÜÓ	ÚÓÜÆΜÜÓ	ÚÓÜÆΜÜÓ	ÚÓÜÆΜÜÓ

ÚÓÜÆΜÜÓ ÚÓÜÆΜÜÓ