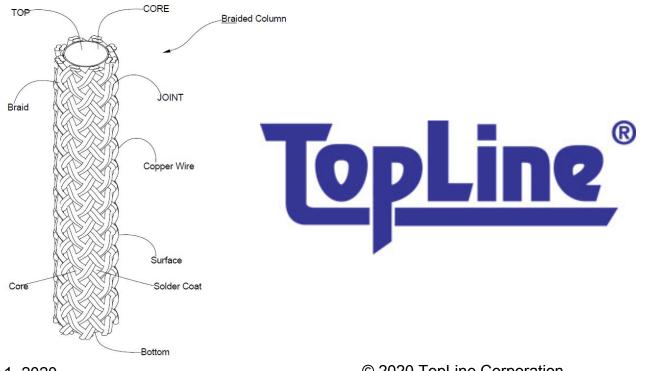
U.S. Patent 10,477,698

New Copper Braided Solder Columns for FPGA and Large Ceramic Modules



Who is TopLine

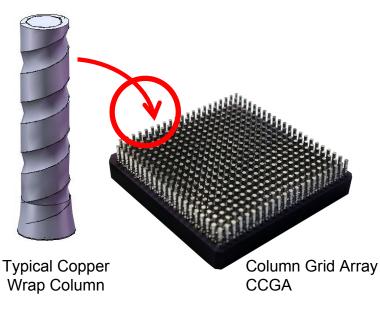
TopLine is a U.S. manufacturer of solder columns for Column Grid Array (CCGA) devices.

TopLine also manufactures daisy chain BGA and CCGA packages, and a wide range of daisy chain semiconductor packages and test vehicle PC boards.

TopLine has assisted thousands of customers in commercial, test, aerospace and defense industries to define and refine their SMT and microelectronics assembly processes throughout the world.



About Copper Wrapped Solder Columns



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Over the past 30 years, the Pb80 Sn20 copper wrap solder column has become accepted as the *preferred* choice for relieving stress due to CTE mismatch between Ceramic Column Grid Array (CCGA) packages and the PCB Board.

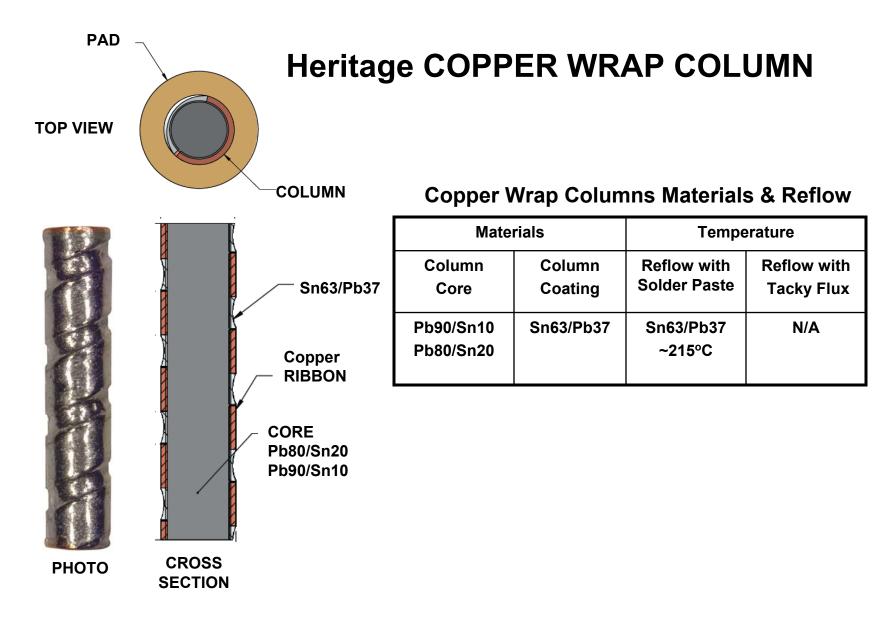
Numerous papers have been written by NASA JPL demonstrating the benefits of packages with solder columns for space missions.

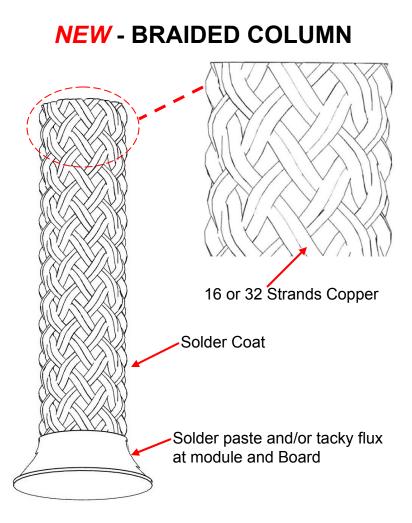
In the past, CCGA package size reduction has been constrained because traditional columns require minimum pad pitch of 1.0mm.

In recent years, plastic grid array IC packages have emerged with fine pitch (~ 0.5mm) and/or very large body sizes exceeding 50mm x 50mm.

The roadmap for fine pitch packages with very high I/O (>10,000) makes it challenging to attach traditional copper wrap solder columns.

TopLine invented a new type of column using copper braiding, rather than copper wrap, to meet the industry's need for smaller pitch and high density CCGA.



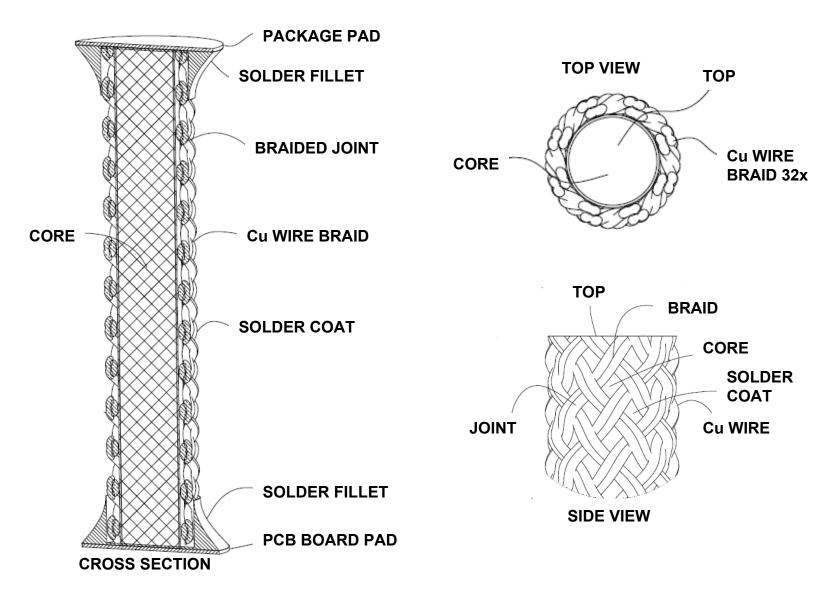


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About Braided Column:

- TopLine invented a new braided solder column with 16 or 32 copper strands exoskeleton sleeve:
- 1) No collapse construction.
- Choice of cores available: Pb80/Sn20, Pb90/Sn10, Sn63/Pb37, Pb93.5/Sn5/Ag1.5 and SAC305 (Pb-Free) and other alloys.
- 3) Roadmap for Air Core and Elastomer Core.
- 4) Column diameter: 0.25mm ~ 0.50mmColumn Length: 1.0mm ~ 4.0mm.
- 5) Pad Pitch 0.5mm to 1.0mm
- 6) Attachment methods: a) Solder Paste orb) Tacky Flux for wetting activation.
- 7) Shorter signal path, lower resistance and improved thermal conduction between module to board.
- 8) Potentially longer life than copper wrap columns (To be tested by NASA & JPL).

SECTION VIEW BRAIDED COLUMN



Comparison New Braided Columns and Heritage Copper Wrap Columns

Attribute	New Braided Column	Heritage Copper Wrap Column	Benefit of Braided Column
Column Diameter	0.25mm ~ 0.50mm 10 ~ 20mil	0.30mm ~ 0.50mm 12 ~ 20mil	Braided columns allows finer pitch with higher density I/O (Pitch: 0.5mm ~ 0.8mm)
Core Material	Pb80/Sn20 Pb90/Sn10 Pb93.5/Sn5/Ag1.5 Sn63/Pb37 SAC305 Air or Peek / Elastomer	Pb80/Sn20 Pb90/Sn10	Braided columns can be attached without collapsing using tacky flux. Alternatively, solder paste can be used to attach Braided Columns.
Signal & Heat Path	16 or 32 strands braided copper non-collapse exoskeleton sleeve.	Single copper ribbon wrapped has a longer helix pattern	Braided columns provide shorter signal path with lower impedance and higher thermal conductivity for transferring heat.
Elasticity	Variety of core materials provides potential for optimizing compliancy	Pb80/Sn20 Pb90/Sn10	Life cycle testing is planned by NASA Marshal Space Flight Center and JPL

New Braided Columns Materials & Reflow

Materials		Temperature	
Column Core	Column Coating	Reflow with Solder Paste	Reflow with Tacky Flux
Pb90/Sn10 Pb80/Sn20	Sn63/Pb37	~215ºC Sn63/Pb37	NA
Pb93.5/Sn5/Ag1.5 HMP-High Melt Point	Pb93.5/Sn5/Ag1.5 HMP-High Melt Point	~310°C Pb93.5/Sn5/Ag1.5	NA
Sn63/Pb37 Eutectic	Sn63/Pb37	~215°C Sn63/Pb37	~215°C
SAC305 Lead Free	SAC305	~235°C SAC305	~235°C
Air, PEEK or Elastomer	Sn63/Pb37 or SAC305	200~240°C Sn63/Pb37 or SAC305	NA

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COPPER BRAIDED COLUMNS

Pitch	arnothing Column	arnothing Pad	
0.5mm	0.30mm	0.40mm	
0.8mm	0.35mm	0.50mm	
1.0mm	0.40mm	0.63mm	
1.0mm	0.50mm	0.80mm	

Choice of Braid Material

- 1) PCC Copper + 2% Pd (Standard)
- 2) Be-Cu Beryllium-Copper for springiness
- 3) Pure Gold Au or Pure Silver Ag

Choice of Core material

- 1) Pb90/Sn10 or Sn63/Pb37
- 2) SAC305 (RoHS) Pb-Free
- 3) Pb93.5/Sn5/Ag1.5 HMP
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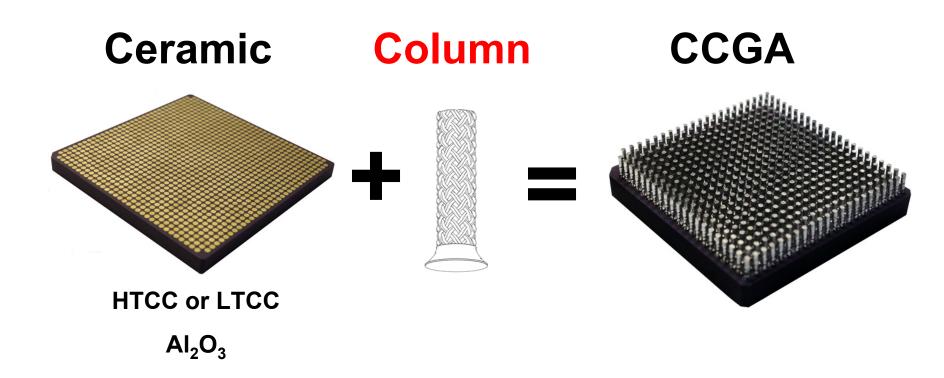
Choice of Coating

- 1) Sn63/Pb37 (Tin-Lead)
- 2) SAC305 Lead Free
- 3) Pb93.5/Sn5/Ag1.5 HMP

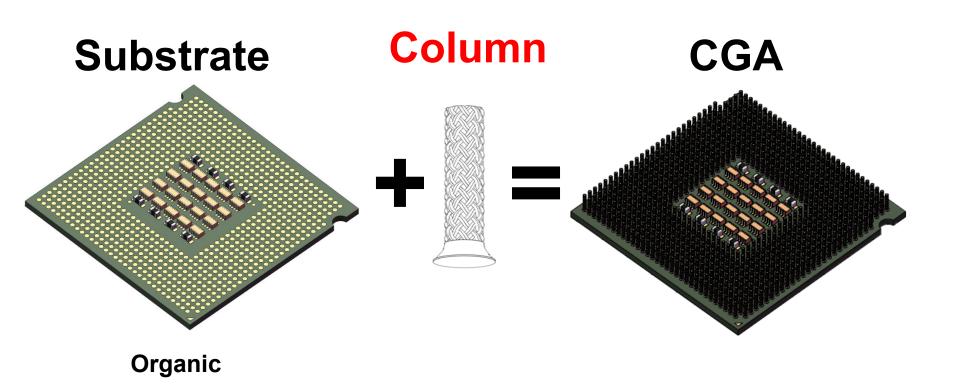
Module / IC Package

- 1) Ceramic
- 2) Organic
- 3) Wafer Level Silicon die

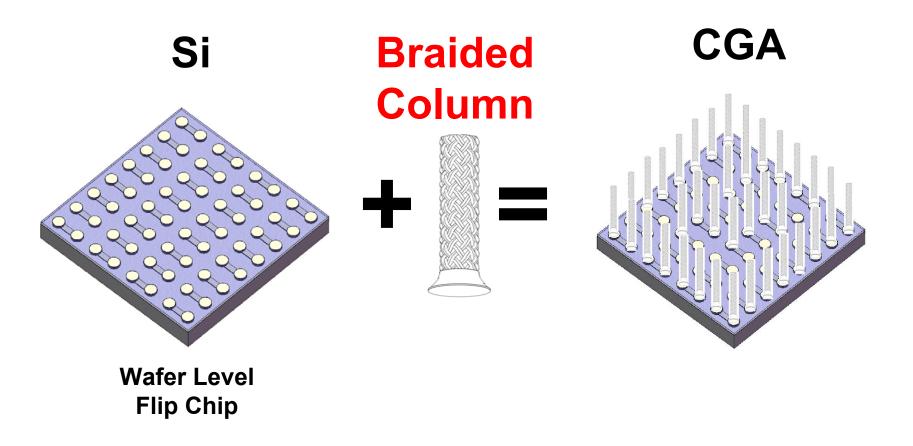
Ceramic Module (FPGA)



Organic (Plastic) Module



Direct Attach Wafer Level - Die



Quick Technical Review: Why Use Columns ?

Cu Wrap COLUMN







Answer:

Columns absorb stress caused by mismatch of Coefficient of Thermal Expansion (CTE) between the module and Board.

When are Columns Required?

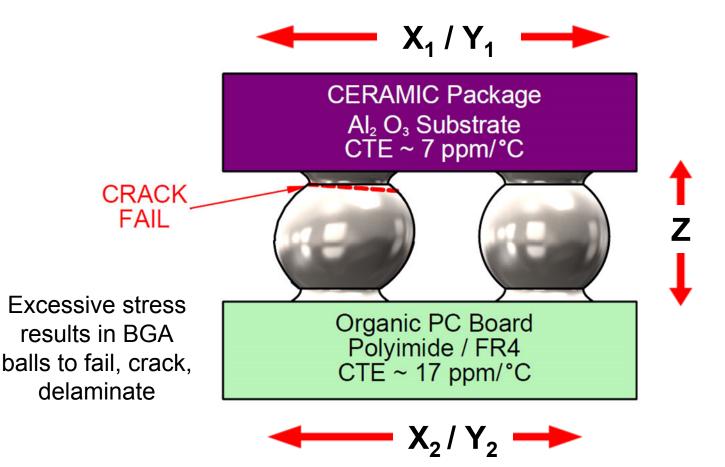
Answer: Large ceramics with wide temperature swings

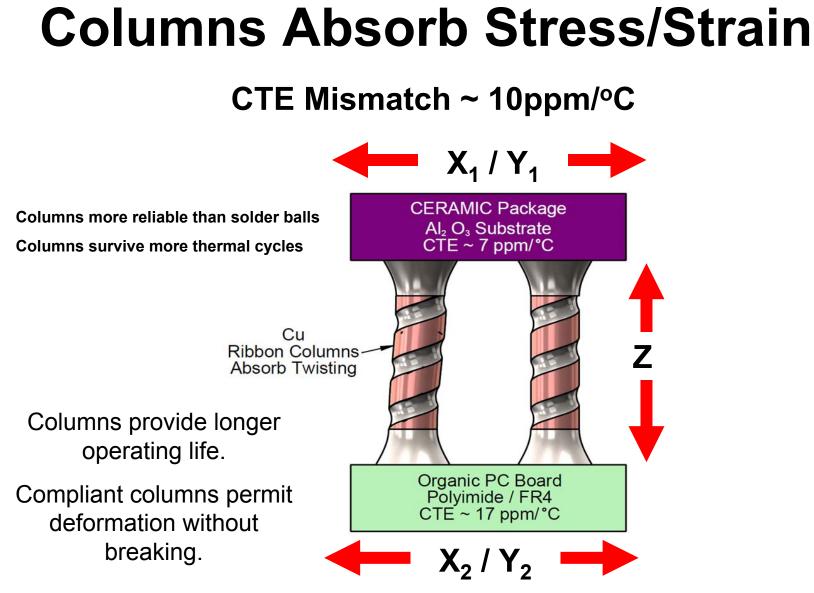
Ball Versus Column With Temperature Swing						
Ceramic	∆ 75⁰C	∆ 100^OC	∆ 150^OC	∆ 175⁰C		
Size	-25° to +50°C	-25° to +75°C	-50° to +100°C	-50° to +125°C		
23 x 23mm	Ball	Ball	Ball or Column	Column		
483 L	12um Strain	16um Strain	25um Strain	30um Strain		
35 x 35mm	Ball	Ball or Column	Column	Column		
1152 L	18um Strain	25um Strain	37um Strain	45um Strain		
52.5 x 52.5mm	Ball or Column	Column	Column	Column		
2577 L	28um Strain	36um Strain	55um Strain	64um Strain		

CAUTION



Solder balls fail due to excessive stress. CTE Mismatch (Ceramic to FR4) ~ 10ppm/°C





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Strain Formula Equation:

Deformation of a solid due to stress.

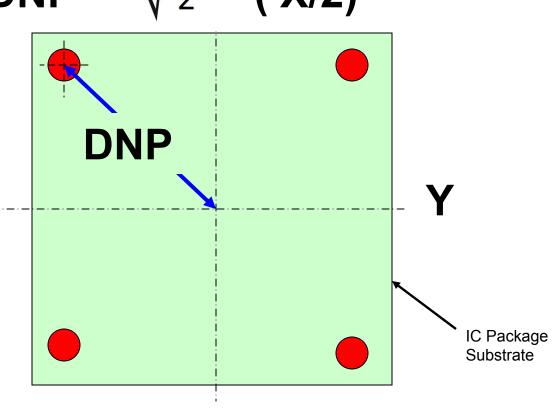
$\Delta \mathbf{x} = \mathbf{DNP} * \Delta \mathbf{T} * (\mathbf{CTE}_1 - \mathbf{CTE}_2)$

DNP – Distance from Neutral Point ΔT = Temperature Change CTE₁ FR4 = 17.5 ppm/°C @25°C CTE₂ Ceramic = 7.5 ppm/°C @25°C

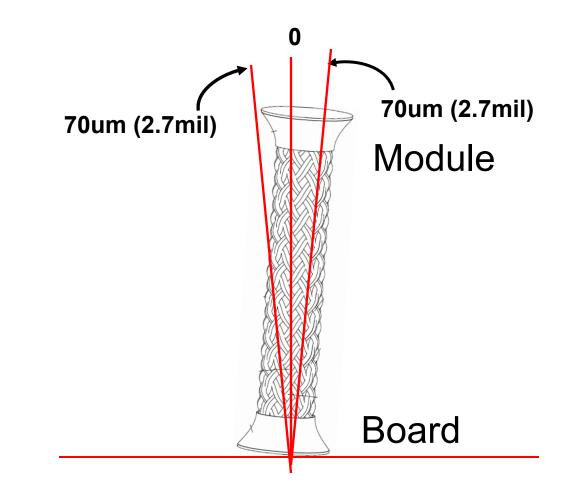
Distance from Neutral Point

DNP = $\sqrt{2}$ * (X/2)

Pads in the corners of the substrate (red color) are the furthest distance from the neutral point (center of package) and are most vulnerable to failure mode



Compliancy Requirement 100 x 100mm Ceramic Substrate





TOPLINE CORPORATION www.TopLine.tv/CCGA.html

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